



**VEMU INSTITUTE OF TECHNOLOGY**

P.Kothakota, Chittoor District -517112

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**Workshop for faculty**

**on**

**“Low Power VLSI  
Design Using Tanner  
EDA Tools”**

**22-08-22 to 23-08-22**

**(Under MOU Activity)**

## ABOUT VEMU IT

VEMU Institute of Technology is one of the well known and finest technical institutions in Chittoor District, A.P. Beginning its quality inputs in 2008, it has attained ISO 2015:9001 certification for quality management within a short span of time. It is located on Tirupati - Chittoor highway on a beautiful 16 acre campus.

A diverse, talented community united by a passion for learning and quest for more, VEMU challenges its students to make a difference on its campus, in the region, and in the companies where ever they join. With unmatched results in every sphere of Education, its commitment to be the best has grown to transform its departments into centre of Excellence.

## About the Program:

The Purpose of workshop is to learn Low Power VLSI Design using EDA Tools. In this you will learn hands on sessions on

- Introduction
- Need for low power design
- Impact of power dissipation
- Low power design methodology
- Power reduction through process technology

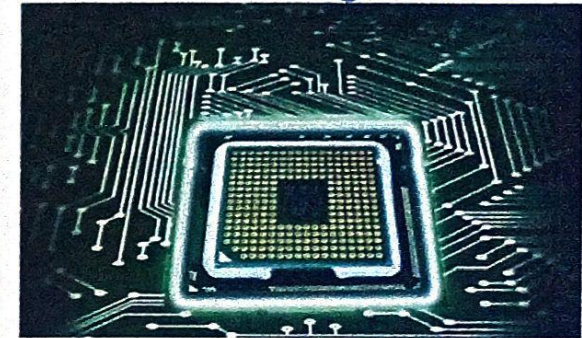
## RESOURCE PERSONS:

**Mr. P Rohith,**  
Technical Engineer,  
Techfluent Solutions Pvt Ltd  
Hyderabad.

**Mr. S Tharun,**  
Application Engineer,  
Techfluent Solutions Pvt Ltd  
Hyderabad.

## TWO DAY WORKSHOP On “LOWER POWER VLSI DESIGN USING TANNER EDA”

22<sup>nd</sup> – 23<sup>rd</sup> August 2022



Organized by  
Department of Electronics  
and Communication  
Engineering,  
(MOU Activity)



VEMU Institute of Technology,  
P.Kothakota, Near Pakala,  
Chittoor District,  
Andhra Pradesh-517112.

**TWO DAY WORKSHOP**  
On  
**“LOWER POWER VLSI DESIGN  
USING TANNER EDA”**  
Registration Form

Full Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Organization: \_\_\_\_\_

Address: \_\_\_\_\_  
\_\_\_\_\_

Mobile No: \_\_\_\_\_

Date: \_\_\_\_\_

Declaration: the information provided is true to best of my knowledge if selected I agree to abide by the rules and regulation I agree to abide by the rules and regulation of the Course and attend the course for entire duration

Place: \_\_\_\_\_

Date: \_\_\_\_\_

**About Department of ECE**

The department offers undergraduate programme in Electronics and Communication Engineering. The Department has highly qualified, experienced and competent faculty members in the areas of Digital Image Processing, Applied Electronics, Digital Electronics, Microwave Communication, and VLSI & Embedded Systems. The department maintains several equipped laboratories for basic and advanced Electronics and Communications. The department has a separate Computer Center with Core2 Duo systems equipped with software applications and suites such as MATLAB, XILINX ISE and Multi-Sim packages. The department has a number of facilities with a substantial collection of teaching tools, instruments, equipment and computers for conducting experiments in different domains of Electronics and Communications Engineering.

**Chief Patron**

Prof.Dr. K.Chandrasekhar Naidu, Chairman

**Patrons**

Dr.Naveen Kilari,  
Principal

**Convener**

Dr S Leela Lakshmi,  
Prof&HOD/ECE

**Co-convener**

Mr.C Manikanta  
Asst.Professor /ECE  
Department of ECE,  
Vemu Institute of Technology,  
P.Kothakota-517112,  
Pakala, Chittoor (Dist),Andhra Pradesh.  
Email ID: hodece@vemu.org  
Mobile:8125169769.

**Eligibility:**

This Workshop is open to faculty members of AICTE approved Engineering colleges, Research Scholars working in various fields of Electronics and Communication Engineering, persons from Industry, R & D organizations and PG scholars who have a genuine interest in the field Embedded systems

**Registration:**

- No registration fee/-
- The number of seats for the Workshop is limited to 30 and selection is based on first come first serve basis
- Registration link:  
<http://vemu.org/workshop.in/event-672>

Last date for filled in application: 01-05-2017

**Accommodation**

- boarding and lodging will be provided for outstation participants on payment basis

**Location Map**

Vemu Institute of Technology is located midway between Tirupati and Chittoor on the Highway. There are direct trains / buses to Tirupati or Chittoor from important cities like Hyderabad, Bangalore and Chennai. It is 4 hours journey by road from Bangalore, 3 hours from Chennai, 1 hour from Tirupati and 30 minutes from Chittoor.

**Route Map to Vemu Institute of Technology**

Tirupathi ← 40Km → 20Km → Chittoor



Vemu IT, P.Kothakota



# VEMU INSTITUTE OF TECHNOLOGY

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[www.vemu.org](http://www.vemu.org)

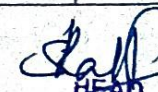
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

## A Two Days Workshop on "Low Power VLSI Design Using Tanner EDA Tools"

On  
22<sup>nd</sup> to 23<sup>rd</sup> August 2022

S.No	Date	Time	Topics to be Covered	Resource Person
1	22/08/2022	10.00AM to 11AM	Inauguration	
		11.00AM to 1 PM	Introduction to VLSI design	Mr.P. Rohith
		1.50PM to 4 PM	Need for low power design, Impact of power dissipation	Mr.P. Rohith
2	23/08/2022	10.00AM to 1PM	Low power design methodology	Mr.S.Tharun
		1.50PM to 3 PM	Power reduction through process technology	Mr.S.Tharun
		3.00PM to 4 PM	Valedictory	

  
Co-Ordinator

  
HEAD  
DEPARTMENT OF ECE  
VEMU INSTITUTE OF TECHNOLOGY  
P. KOTHAKOTA - 517 112



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www.vemu.org

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ATTENDANCE SHEET - WORKSHOP

S.No.	Name of the Participant	22/08/2022		23/08/2022	
		FN	AN	FN	AN
1.	Dr. S.MuniRathnam				
2.	Dr.G.Elaiyaraja				
3.	Dr. S. Leela Lakshmi				
4.	Mr.G.Sankarappa				
5.	Mrs.N.Prakash Babu				
6.	Mrs. R.Rani				
7.	Mrs. K.JayaSree				
8.	Mr. Y.Mahesh				
9.	Mr. N.Kiran Kumar				
10.	Mr. .P.H.ChandraMouli				
11.	Mrs.P.Tejaswini				
12.	Mr. P.Vemaiah				
13.	Mrs. P.Sirisha				
14.	Ms. V.Bharathi				
15.	Mr. B.Praveen Kumar				
16.	Mrs. J.V.Pesha				
17.	Mr. D.Mohan Reddy				
18.	Mrs.R.Sindhu				
19.	Mr. N.Gangadharam				
20.	Mr.M.Arun Kumar				
21.	Mrs.P. Sireesha				

22	Mr.G.SivaKoteswar Rao	GSR	GSR	GSR	GSR
23	Mr.C.Manikanta	GP	GP	GP	GP
24	Mr. M. Gopi	GP	GP	GP	GP
25	Ms. C. Bhagya	B	B	B	B
26	Mr. R. Narayana Rao	RNR	RNR	RNR	RNR
27	Mr. M.P. Chennaiah	CP	CP	CP	CP
28	Mr. T.K.Kalaiarasan	TKK	TKK	TKK	TKK
29	Mrs. C. Muni Kantha	CMK	CMK	CMK	CMK
30	Mrs. K.J.Poornima	KJP	KJP	KJP	KJP
31	Mrs.S. Vidya	SV	SV	SV	SV

  
CO-ORDINATOR

  
HEAD  
CONVENER  
DEPARTMENT OF ECE  
VEMU INSTITUTE OF TECHNOLOGY  
P. KOTHAKOTA - 517 112



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

**Report-on**

**Workshop**

**on**

## **“Low Power VLSI Using Tanner EDA Tools” (MOUActivity)**

A Two days workshop on “Low Power VLSI using Tanner EDA Tools” was conducted by department of ECE by inviting Sri. P Rohith, Technical Engineer-Techfluent Solutions Pvt.Ltd, Hyderabad from 22-08-2022 to 23-08-2022. The main objective of this workshop is to provide hands on experience with Tanner EDA Tools.

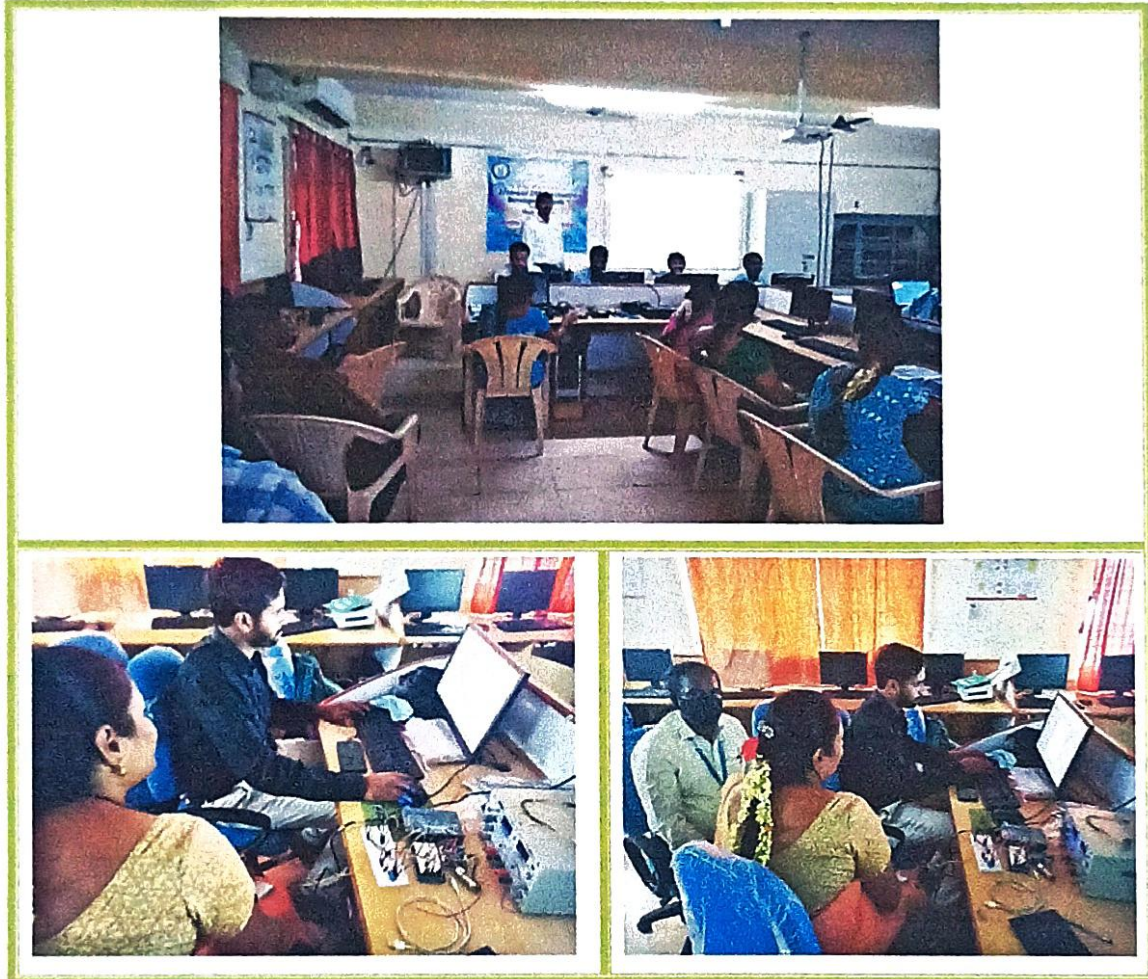
**Day-1(22-08-2022)**, The programme was inaugurated by ECE Dept, VEMU IT .Theworkshop Programme was started with Dr. S. Leela Lakshmi, HOD of ECE, had welcomed Resource person Sri. Sri. P Rohith, Technical Engineer -Techfluent Solutions Pvt.Ltd, Hyderabad , Principal Dr. Naveen Kilari and Vice Principal Dr.D.Chandra sekhar. He had given introduction of all Dignitaries of the programme and invited guest. Dr. Naveen Kilari, Principal shared his views about the program. He informed that these kinds of workshop Programmes improve the knowledge in teaching. He congratulated the Department for organizing such a program.

After inauguration Resource person Sri. P Rohith started his session by introduction to Introduction to VLSI Design and Need for low power design, Impact of power dissipation.

**Day-2 (08-02-2022)**, Resource person discussed about Low power design methodology, Power reduction through process technology

In this workshop the faculty members participated actively by asking doubts and the resource persons also clarified their doubts patiently.

## Photo gallery



Finally, the workshop was ended by Valedictory. The resource person concluded the program by enhancing the knowledge on Tanner EDA Tools. The total program successfully completed with the cooperation of Principal, HOD, and all the faculty members of the department.

  
Head of the Department  
DEPARTMENT OF ECE  
VEMU INSTITUTE OF TECHNOLOGY  
P. KOTHAKOTA - 517 112



# VEMU INSTITUTE OF TECHNOLOGY

P.KOTHAKOTA, NEAR PAKALA, CHITTOOR-517112

## Department of Electronics and Communication Engineering Certificate of Participation


This is to certify that

Ms. C. BHAGYA

of

VEMU I.T

has actively participated in Two Days Workshop on "Low Power VLSI Design Using EDA Tools" Organized by the Department of Electronics and Communication Engineering, VEMU Institute of Technology, P.Kothakota, In Association with Techfluent Solutions Pvt. Ltd., Hyderabad, from 22<sup>nd</sup> & 23<sup>rd</sup> AUG 2022 .

  
Co-Ordinator

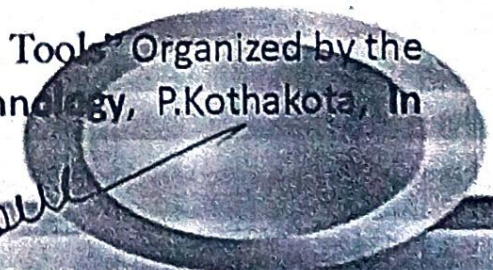
Mr. C. Manikanta

  
PRINCIPAL  
VEMU INSTITUTE OF TECHNOLOGY  
P. KOTHAKOTA - 517112

HOD

Dr. S. Leela Lakshmi

  
Principal  
Dr Naveen Kilari





# VEMU INSTITUTE OF TECHNOLOGY

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## Department of Electronics and Communication Engineering Certificate of Participation

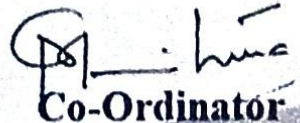
This is to certify that

Mrs. K. JAYASREE

of

VEMU IT

has actively participated in Two Days Workshop on "Low Power VLSI Design Using EDA Tools" Organized by the Department of Electronics and Communication Engineering, VEMU Institute of Technology, P.Kothakota, in Association with Techfluent Solutions Pvt. Ltd., Hyderabad, from 22<sup>nd</sup> & 23<sup>rd</sup> AUG 2022 .

  
Co-Ordinator

Mr. C. Manikanta

  
PRINCIPAL  
VEMU INSTITUTE OF TECHNOLOGY  
P. KOTHAKOTA - 517 112

Dr. S. Leela Lakshmi

  
HOD

  
Principal  
Dr Naveen Kilari