



VEMU INSTITUTE OF TECHNOLOGY

P.Kothakota, Chittoor District -517112

Four Day

FDP

on

**“Cadence Tools & VLSI
Design”**

20.5.19 to 24.5.19

(Under MoU Activity)

Vemu Institute of Technology

Affiliated to JNTUA, Anandapuram

REGISTRATION FORM

Faculty Development Programme

On

CADENCE TOOLS & VLSI DESIGN

Name: _____

College: _____

Branch: _____

Address for Correspondence: _____

Phone No: _____

E-mail Id: _____

Registration fee: 400/-

Signature of Candidate

Signature of the Sponsoring Authority with Seal

Chief Patron

Dr. K. Chandrasekhar Naidu
Chairman

Patron

Dr. T. Chandrasekhar Rao
Principal

Convener

Dr. S. Munirathnam
HOD, Dept. OF ECE

ORGANIZING COMMITTEE:

• **Dr. A Pulla Reddy**

Ph.D. Professor

• **Mrs. B Saroja**

M.Tech, Associate Professor

• **Mr. R Mallikarjuna Reddy**

M.Tech, Associate Professor

• **Mrs. M Poornima**

M.Tech, Associate Professor



Vemu Institute of Technology
P.Kothakota

Organizes

Faculty Development Programme

On

CADENCE TOOLS & VLSI DESIGN

from

MAY 20-24, 2019



VEMU

INSTITUTE OF TECHNOLOGY

(Affiliated to JNTUA, Ananthapuram)

P.Kothakota, Chittoor - 517112.

E-Mail: vemucedept@gmail.com

Contact: -5596661104

VENU PROFILE

Venu is one of the ISO 9001-2015 certified premier Engineering Management colleges in the region of Rayalaseema of A.P. Established in 2003 (located on the Chittoor - Tirupati Highway), approved by AICTE and affiliated to JNTUA.

VISION OF THE DEPARTMENT:

To develop as a center of excellence in electronics & communication engineering field and produce graduate with knowledge of core as well as interdisciplinary domain, cutting edge technologies of the industry and research along with human values and professional ethics to address the global and science challenges.

MISSION OF THE DEPARTMENT

Provide the students an environment of academic freedom that will insure the exchange of ideas and knowledge through effective teaching

Enable students to develop skills to solve complex technological problems of current times and also provide a framework for promoting collaborative

Provide industry-institute interaction training to the students that bridges the gap between industry and academic along with ethics and values

To provide logical and analytical skills by providing solid foundation in basic sciences, mathematics, Electronics and Communication Engineering

RESOURCE PERSON:

Dr. C.N. Krishnan,

Director, Geen Comor Tech, Nellore

Dr. Varun Gaiikwad,

Senior Engineer, Entuple Technologies, Bangalore



ELIGIBILITY:

Faculty of Engineering Colleges & PG Colleges offering B.Tech, M.Tech.

GENERAL INFORMATION:

No. of participants limited to 70

Accommodation will be provided on payment basis

Free transportation from tirupati & chittoor.

Participation certificate will be issued

CONTACT PERSON:

Dr. S.MUNIRATHNAM, Convener

Mobile : 888 66 61 104

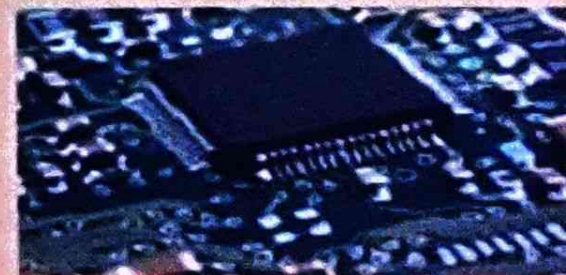
venuacedept@gmail.com

IMPORTANT DATES:

Last date for the receipt of filled in application 20.05.2019

FDP DATE :

FDP Date: 20th-24th May 2019





VEMU INSTITUTE OF TECHNOLOGY

P. KOTHAKOTA, TIRUPATHI-CHITTOOR ROAD, CHITTOOR, AP-517112
(Permanently Affiliated to JNTUA Ananthapuramu, Approved by AITE New Delhi, & ISO 9001:2015 Certified Institution)

DEPARTMENT OF E.C.E

FDP Schedule

Title: "Cadence Tool & VLSI Design"

Date: 20/05/2019-24/05/2019

Day	Time	Topics Covered	Resource Person
Day 1 20/05/2019	9.30 AM- 10.00 AM	Inaugural Session	
	10.00 AM- 12.00 PM	Introduction to Cadence tool & VLSI Design	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
	1.30 PM- 4.30 PM	Digital circuits Background	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
Day 2 21/05/2019	9.30 AM- 12.00 PM	CMOS Basics	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
	1.30 PM- 4.30 PM	CMOS layout Design	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
Day 3 22/05/2019	9.30 AM- 12.00 PM	CMOS layout Design	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
	1.30 PM- 4.30 PM	MOS modeling	Dr.C.N.Krishnan, Director, Green Corner Tech, Nellore
Day 4 23/05/2019	9.30 AM- 12.00 PM	VHDL - Verilog basics	Mr. Varun Gaikwad, Senior Engineer, Entuple Technologies, Bangalore
	1.30 PM- 4.30 PM	Statistical analysis	Mr. Varun Gaikwad, Senior Engineer, Entuple Technologies, Bangalore
Day 5 24/05/2019	9.30 AM- 12.00 PM	Pull up and Pull down transistors	Mr. Varun Gaikwad, Senior Engineer, Entuple Technologies, Bangalore
	1.30 PM- 3.00 PM	Overview on Cadence tool & VLSI Design	Mr. Varun Gaikwad, Senior Engineer, Entuple Technologies, Bangalore
	3.00 PM - 4.30 PM	Valedictory Session	

G. S. Srinivas
Co-Convener

[Signature]
Convener
DEPARTMENT OF ECE
VEMU INSTITUTE OF TECHNOLOGY
P. KOTHAKOTA - 517 112

14.	Mrs. B.Saroja	BS	BS	BS	BS	BS	BS	BS	BS	BS	BS
15.	Mrs. C.Geetha	G	G	G	G	G	G	G	G	G	G
16.	Mr. N.Kiran Kumar	NK	NK	NK	NK	NK	NK	NK	NK	NK	NK
17.	Mrs. S.Chandana	CA	CA	CA	CA	CA	CA	CA	CA	CA	CA
18.	Mr. K.Rohith	RC	RC	RC	RC	RC	RC	RC	RC	RC	RC
19.	Mrs. D.Reena	RC	RC	RC	RC	RC	RC	RC	RC	RC	RC
20.	Ms. J.V.Pesha	JV	JV	JV	JV	JV	JV	JV	JV	JV	JV
21.	Mrs. M.Bhargavi	MB	MB	MB	MB	MB	MB	MB	MB	MB	MB
22.	Mr. .P.H.ChandraMouli	PH	PH	PH	PH	PH	PH	PH	PH	PH	PH
23.	Mr. P.Vemaiah	VH	VH	VH	VH	VH	VH	VH	VH	VH	VH
24.	Mrs. P.Sirisha	PS	PS	PS	PS	PS	PS	PS	PS	PS	PS
25.	Mrs.C. Sai Pallavi	SP	SP	SP	SP	SP	SP	SP	SP	SP	SP
26.	Mrs. V.Bharathi	VB	VB	VB	VB	VB	VB	VB	VB	VB	VB
27.	Mrs. M.J.Chithraja	CA	CA	CA	CA	CA	CA	CA	CA	CA	CA

P. stanni
CO-ORDINATOR

[Signature]
HEAD
CONVENOR
DEPARTMENT OF ECE
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P. KOTHAKOTA - 517 112



VEMU INSTITUTE OF TECHNOLOGY
P.KOTHAKOTA, TIRUPATHI-CHITTOOR ROAD, CHITTOOR, AP-517112
DEPT OF ELECTRONICS & COMMUNICATION ENGINEERING

Report-on

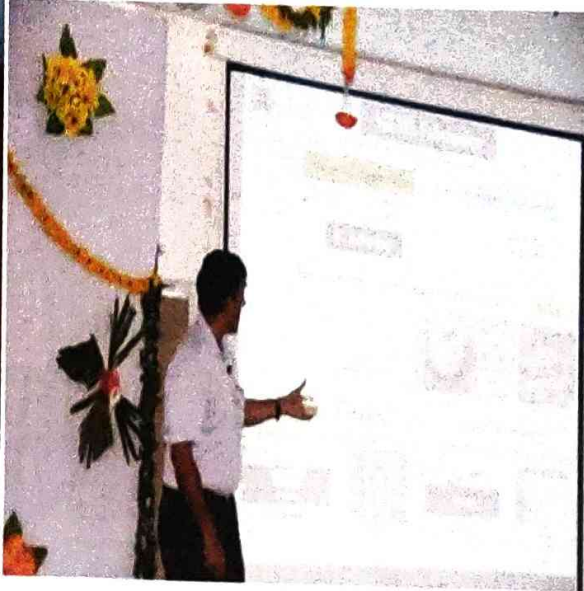
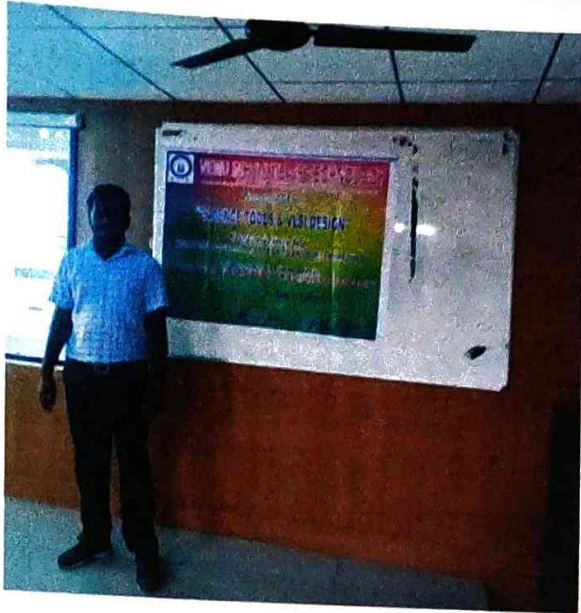
Cadence Tools & VLSI Design

The programme was inaugurated by Ece Dept, VEMU IT. The Guest and Speaker of this programme is Dr. C.N.Krishnan, Director, Green Corner Tech, Nellore on May20, 2019. The objective of this Faculty Development Programme is to give a better exposure to the faculty members on the subject "VLSI DESIGN". In first day FDP, the speaker explained about the Introduction to VLSI Design and Cadence Tool and he discussed about the Digital circuits background. On second & third day, he delivered an illuminating and inspiring lecture on very important aspects of VLSI Technology like VLSI overview, Design, Recent trends, Device Simulation, IC fabrication, CMOS layout Design, VHDL and its programming etc.

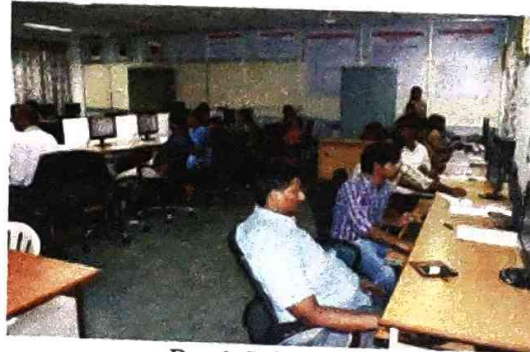
Dr. Varun Gaikwad, senior Engineer, Entuple Technologies hand over the session on 23rd & 24th May 2019. He explained about the VHDL-Verilog basics, pull up and pull down transistors and overview on VLSI. The course covers detailed theory, tutorials and laboratory practices. Topics related to INTRODUCTION TO CMOS TECHNOLOGY, CIRCUIT CHARACTERIZATION AND SIMULATION, COMBINATIONAL AND SEQUENTIAL CIRCUIT DESIGN, CMOS TESTING, and SPECIFICATION USING VERILOG HDL were covered. As it is the major key factor for present emerging systems and technology. For this FDP 30 members are attended. This FDP gave hand on experience for faculty to update themselves in the area of VLSI Design. This programme provided an in-depth knowledge on both theory and hands –on training for industrial based tool like Cadence.



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DEPT OF ELECTRONICS & COMMUNICATION ENGINEERING



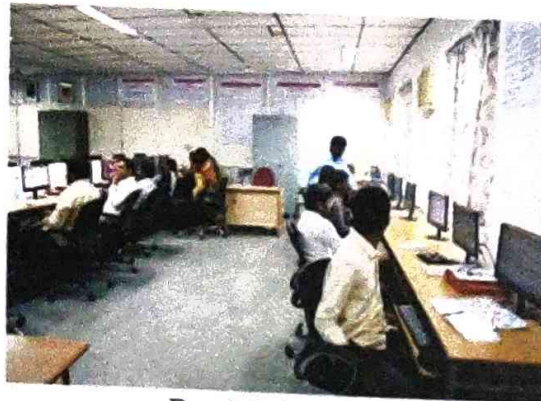
Addressing by Resource person



Day 1: Lab session



Day 5: Lab session



Day 4: Lab session

G. Srinivas
 Faculty In-charge

[Signature]
 Head of the Department
HEAD
DEPARTMENT OF ECE
VEMU INSTITUTE OF TECHNOLOGY
 P, KOTHAKOTA - 517 112

[Signature]
HOD-ECE

[Signature]
 Director





VEMU INSTITUTE OF TECHNOLOGY

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Accredited by NAAC and ISO 9001:2015 Certified Institution)
Tirupati-Chittoor Highway road, P.Kothakota, Chittoor-517112

Certificate

This is to certify that Dr./Prof./Mr./Ms. N. KIRAN KUMAR

From VEMU IT participated in
Faculty Development Programme on “**CADENCE Tools &
VLSI Design**” held between 20th to 24th May 2019, Organized
by Dept. of Electronics and Communication Engineering, in
association with **Green Corner Technologies, Nellore.**


PRINCIPAL
VEMU INSTITUTE OF TECHNOLOGY
P. KOTHAKOTA - 517 112


Co-ordinator


HOD-ECE



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