



VEMU INSTITUTE OF TECHNOLOGY::P.KOTHAKOTA

NEAR PAKALA, CHITTOOR-517112

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

Department of Electrical & Electronics Engineering

VEMU/EEE/2017-18

Date: 30-08-2017

Circular

It is hereby informed to all the EEE Lab Technicians that there is a Two days Workshop on “**Designing circuits & Testing in Electrical Engineering**” from 06/09/2017 - 07/09/2017 by inviting D.Chandrasekhar, HOD, Dept. of EEE, Vemu institute of technology, M.Murali, Assoc.prof, Dept. of EEE, Vemu institute of technology, P.kothakota. Hence all the EEE Lab Technicians are instructed to attend the workshop without fail.

Venue: EEE Seminar hall

Time: 10.00am-1.00pm

1.50pm-4.00pm

Copy To: The Principal Desk,
The Staff In-charge,
Circulate among all Lab Technicians.

A. Judd
V.P. Sharma
Rekha
F. Yerrach
G. Nishan Ahamed

Ch
**HOD
HEAD
DEPARTMENT OF EEE
VEMU INSTITUTE OF TECHNOLOGY
P. KOTHAKOTA - 517112**



VEMU INSTITUTE OF TECHNOLOGY::P.KOTHAKOTA

NEAR PAKALA, CHITTOOR-517112

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

Department of Electrical & Electronics Engineering

PROGRAM SCHEDULE

Two days Workshop on "Designing circuits & Testing in Electrical Engineering"

Date(s) : 06/09/2017 & 07/09/2017

Time: 10.00 AM to 04.00 PM

Date	Forenoon (10.00am-1.00pm)	Afternoon (1.50pm-4.00pm)
06/09/2017	Designing the basic RLC circuits.	Testing the basic RLC circuits.
07/09/2017	Designing the basic electronics circuits.	Testing the basic electronics circuits.


HOD / EEE

HEAD
DEPARTMENT OF EEE
VEMU INSTITUTE OF TECHNOLOGY
P. KOTHAKOTA - 517 112



VEMU INSTITUTE OF TECHNOLOGY::P.KOTHAKOTA
NEAR PAKALA, CHITTOOR-517112
DEPT OF ELECTRICAL & ELECTRONICS ENGINEERING

TWO DAYS WORKSHOP on "Designing Circuits & Testing in Electrical Engineering"

Attendance Sheet

S.No.	Name of the Participant	06/09/2017		07/09/2017	
		FN	AN	FN	AN
1.	Mr.V.Rakesh Sharma	V.R Sharma	V.R Sharma	V.R Sharma	V.R Sharma
2.	Mr.P.Yerraiah	P.Yerraiah	P.Yerraiah	P.Yerraiah	P.Yerraiah
3.	Mrs.T.Rekha	T.Rekha	T.Rekha	T.Rekha	T.Rekha
4.	Mr.A.Jyothi Kishore	A.Jyothi	A.Jyothi	A.Jyothi	A.Jyothi
5.	Mr.G.Nishar Ahamed	G.Nishar Ahamed	G.Nishar Ahamed	G.Nishar Ahamed	G.Nishar Ahamed

A. Ashu
Co-ordinator


HOD/EEE
HOD
Electrical & Electronics Engg.
Vemu Institute of Technology
P. Kothakota



VEMU INSTITUTE OF TECHNOLOGY


NEAR PAKALA, CHITTOOR-517112

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

PARTICIPATION CERTIFICATE

This is to certify that Mr/Ms/Miss A. Jyothi kishor
of EEE department has actively participated in Two days Workshop
on “Designing circuits & Testing in Electrical Engineering” during 06/09/2017 &
07/09/2017 organized by Department of Electrical & Electronics Engineering, Vemu
Institute of Technology, P.Kothakota.


Dr.D.Chandrasekhar
HOD-EEE


Dr.D.Obulesu
Principal



VEMU INSTITUTE OF TECHNOLOGY


NEAR PAKALA, CHITTOOR-517112

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

PARTICIPATION CERTIFICATE

This is to certify that Mr/Ms/Miss G. Nishar Ahamed
of EEE department has actively participated in Two days Workshop
on “Designing circuits & Testing in Electrical Engineering” during 06/09/2017 &
07/09/2017 organized by Department of Electrical & Electronics Engineering, Vemu
Institute of Technology, P.Kothakota.


Dr. D. Chandrasekhar
HOD-EEE


Dr. D. Obulesu
Principal



REPORT

Event Type	Offline Staff Skill Development Program
Title of the Event	Two days Workshop on "Designing circuits & Testing in Electrical Engineering"
Dates	06/09/2017 & 07/09/2017
Keynote speaker	D.Chandrasekhar, HOD, M.Murali, Assoc.prof, Dept. of EEE, Vemu institute of technology, P.kothakota
Faculty Coordinator	Dr.A.Hemasekhar
Venue	Simulation Lab
Benefited Persons	EEE Lab technicians (5 Participants)
Description about the Event	<p>The main objective of this Staff Skill Development Program is to provide information about how to design and test the basic RLC circuits and the electronics circuits. Basic testing procedures typically involve anything you can do with a digital multimeter, such as: measure voltage, measure current, measure resistance and measure capacitance. An electrical safety test provides insight into whether or not one's product is safely designed, such that one can avoid or reduce the risk of the product being the cause of damage or injury to people, pets and property. Electronic circuit design consists of analysis and synthesis of electronic circuits. While designing an analog circuit or digital circuit the designer should be able to predict the voltage and current at every node in the circuit. An electrical design engineer is a professional who develops electrical systems such as alarms and lighting. They use vast knowledge of the principles of physics and material sciences to help create solutions for new electrical systems and provide maintenance or installation services to current electrical systems.</p>
Outcome	<ul style="list-style-type: none">❖ Designing the basic RLC circuits.❖ Testing the basic RLC circuits.❖ Designing the basic electronics circuits and❖ Testing the basic electronics circuits.
Photos	

A. Hemasekhar
(Coordinator)


HOD/EEE
HEAD
DEPARTMENT OF EEE
VEMU INSTITUTE OF TECHNOLOGY
P. KOTHAKOTA - 517 112